



arm

RMM unit test framework

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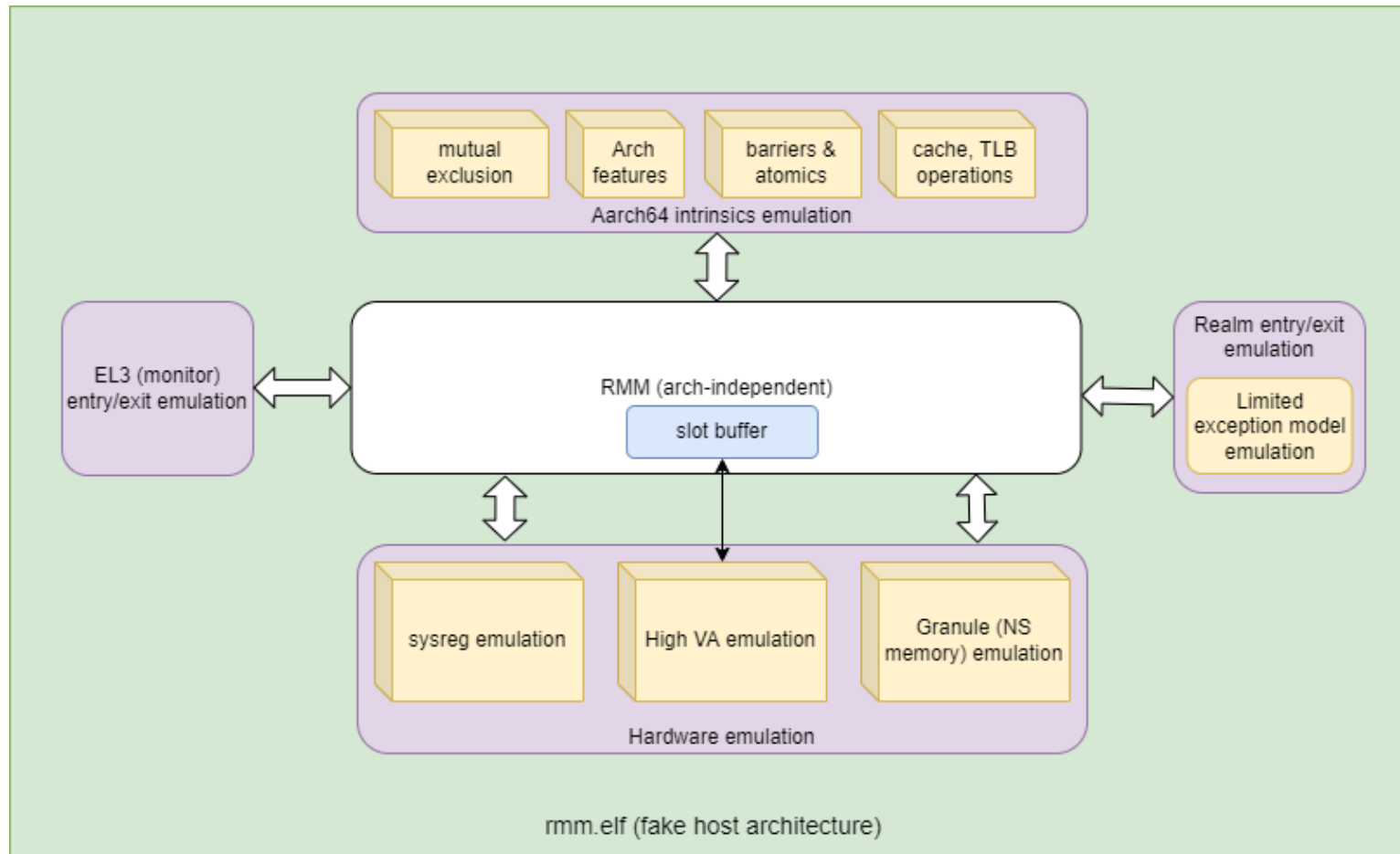
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arm

TF-RMM fake_host architecture

Fake_host architecture for RMM



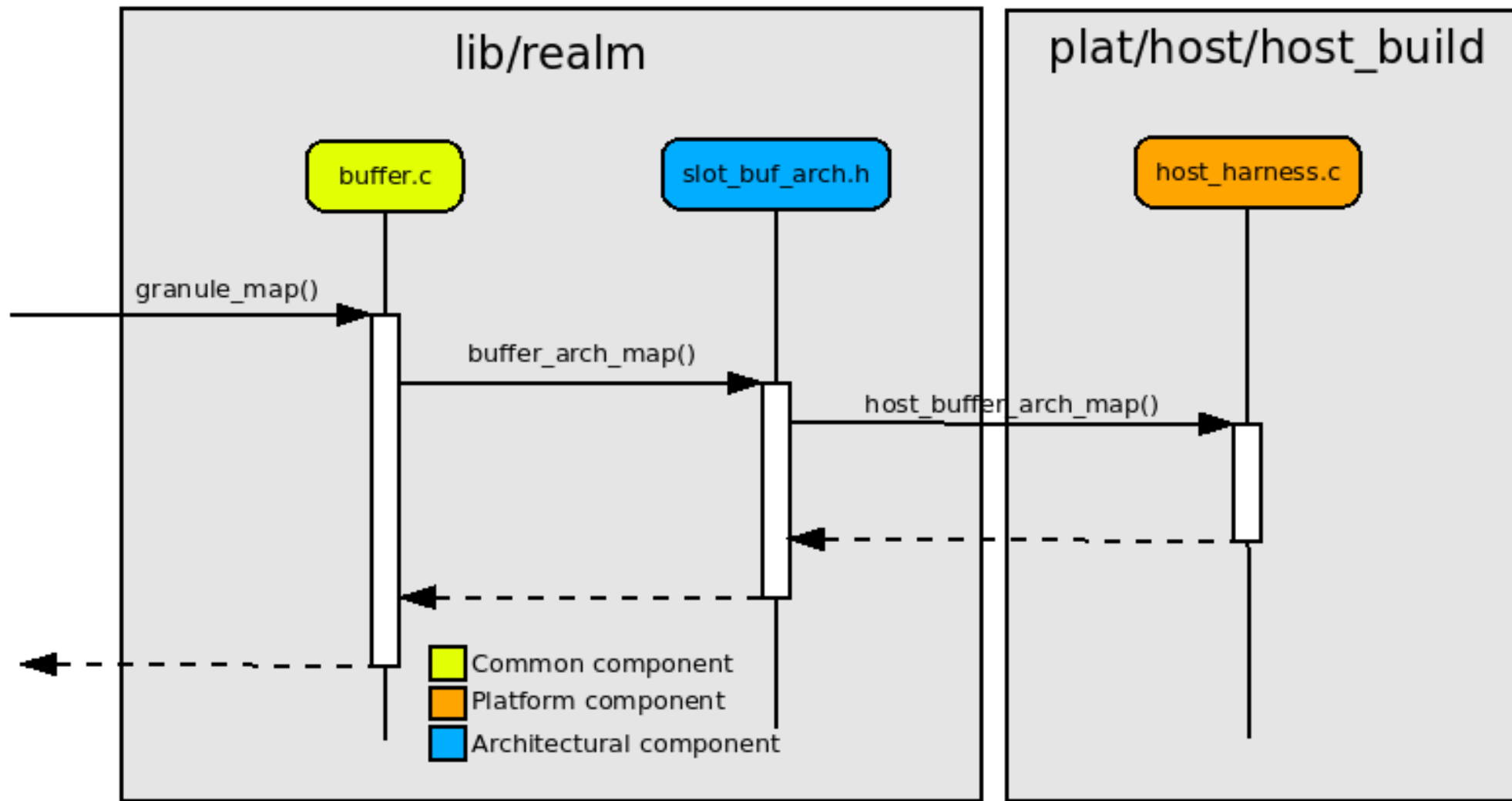
- + Builds and runs the program natively
- + Facilitates development of architecture independent parts of RMM on the host
- + Not intended to support multi-thread
- + Cannot execute AArch64 assembly
- + Emulation for AArch64 exceptions is limited
- + libc implementation in RMM cannot be verified with this architecture.

host platform components

```
▼ plat
  ▼ common
  ▼ include
    C plat_common.h
  ▼ src
    C plat_common_init.c
  M CMakeLists.txt
  ▼ fvp
  ▼ src
  ▼ include
    C fvp_private.h
    C fvp_granule.c
    C fvp_setup.c
  M CMakeLists.txt
  ▼ host
  ▼ common
  ▼ include
    C host_defs.h
    C host_utils.h
  ▼ src
    C host_harness_cmh.c
    C host_platform_api_cmh.c 4
    C host_utils.c
  M CMakeLists.txt
  ▼ host_build
  ▼ src
    C host_harness.c
    C host_setup.c
  M CMakeLists.txt
  M CMakeLists.txt
```

- + *plat/* contains subdirectories for all the different platforms.
 - *fvp/* & *host/*
 - *common/* contains common components for all the platforms, such as platform setup.
- + *host/* is the home for the `fake_host` architecture. It can support different variants

fake_host harness mechanism



fake_host harness mechanism

```
/*
 * SPDX-License-Identifier: BSD-3-Clause
 * SPDX-FileCopyrightText: Copyright TF-RMM Contributors.
 */

#ifndef SLOT_BUF_ARCH_H
#define SLOT_BUF_ARCH_H

#include <host_harness.h>

static void *buffer_arch_map(enum buffer_slot slot,
                             unsigned long addr, bool ns)
{
    return host_buffer_arch_map(slot, addr, ns);
}

static void buffer_arch_unmap(void *buf)
{
    return host_buffer_arch_unmap(buf);
}

#endif /* SLOT_BUF_ARCH_H */
```

fake_host

```
/*
 * SPDX-License-Identifier: BSD-3-Clause
 * SPDX-FileCopyrightText: Copyright TF-RMM Contributors.
 */

#ifndef SLOT_BUF_ARCH_H
#define SLOT_BUF_ARCH_H

#define buffer_arch_map                buffer_map_internal
#define buffer_arch_unmap              buffer_unmap_internal

#endif /* SLOT_BUF_ARCH_H */
```

fvp

Sysreg access emulation on fake_host

Callbacks for sysreg access

- + Provides support to install callback functions that will be called upon sysreg access
- Separate callbacks for read and write access
- Ability to configure a default (reset) value for the register.

```
/*
 * Setup callbacks for sysreg read and write operations.
 *
 * This API allows to setup callbacks for each sysreg to be called upon
 * read or write operations. This allows to control what to return on
 * a read or how to process a write.
 *
 * Arguments:
 *   name - String containing the name of the sysreg. The name of
 *           the sysreg cannot exceed MAX_SYSREG_NAME_LEN (excluding
 *           the terminating null character) or it will be truncated.
 *   rd_cb - Callback to be invoked on a read operation.
 *   wr_cb - Callback to be invoked on a write operation.
 *   init - Value used as reset value for the sysreg.
 *
 * Returns:
 *   0 on success or a negative error code otherwise.
 */
int host_util_set_sysreg_cb(char *name, rd_cb_t rd_cb, wr_cb_t wr_cb,
                           u_register_t init);
```

```
/*
 * Performs some initialization needed before RMM can be ran, such as
 * setting up callbacks for sysreg access.
 */
static void setup_sysreg_and_boot_manifest(void)
{
    /*
     * Initialize ID_AA64MMFR0_EL1 with a physical address
     * range of 48 bits (PARange bits set to 0b0101)
     */
    (void)host_util_set_default_sysreg_cb("id_aa64mmfr0_el1",
                                          INPLACE(ID_AA64MMFR0_EL1_PARANGE, 5UL));

    /*
     * Initialize ICH_VTR_EL2 with 6 preemption bits.
     * (PREbits is equal number of preemption bits minus one)
     */
    (void)host_util_set_default_sysreg_cb("ich_vtr_el2",
                                          INPLACE(ICH_VTR_EL2_PRE_BITS, 5UL));

    /* SCTLR_EL2 is reset to zero */
    (void)host_util_set_default_sysreg_cb("sctlr_el2", 0UL);

    /* Initialize the boot manifest */
    boot_manifest->version = RMM_EL3_IFC_SUPPORTED_VERSION;
    boot_manifest->plat_data = (uintptr_t)NULL;
}
```

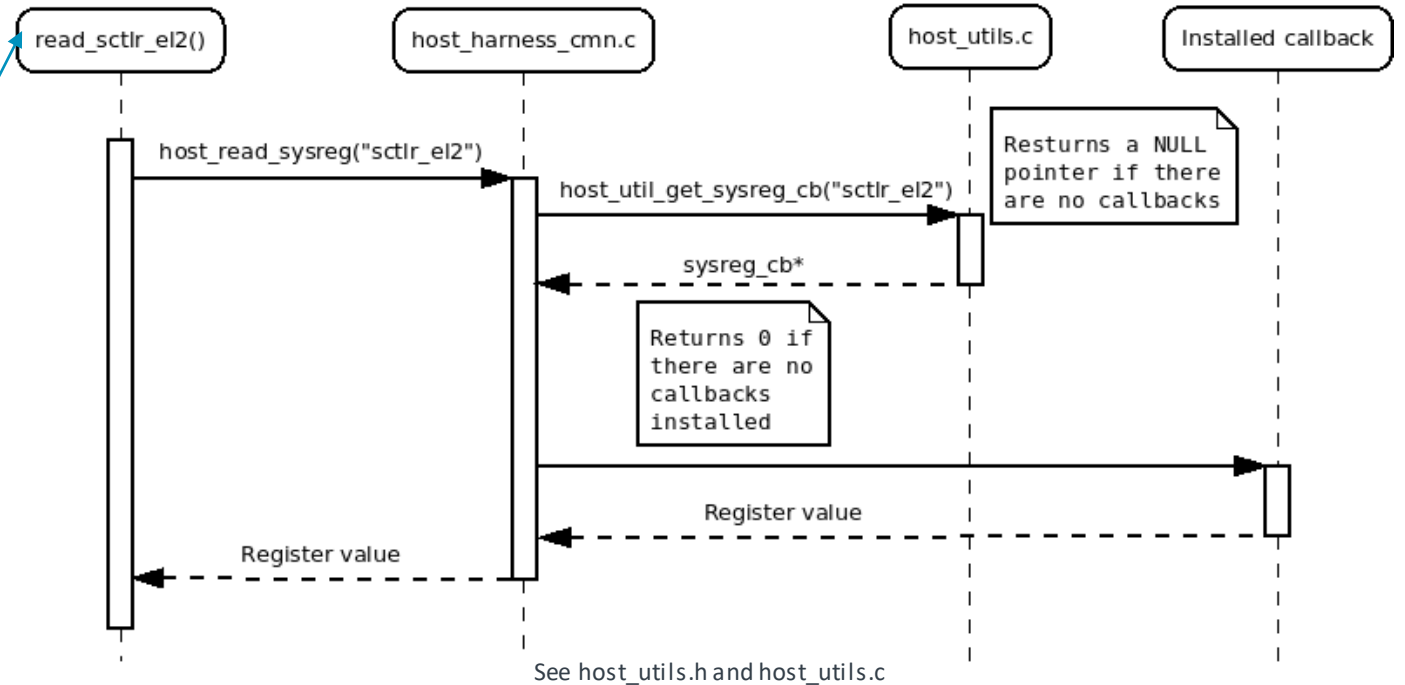

Sysreg access emulation on fake_host

Callbacks for sysreg access

```
DEFINE_SYSREG_RW_FUNCS(sctlr_el1)  
DEFINE_SYSREG_RW_FUNCS(sctlr_el2)
```

```
/* Define read & write function for system register */  
#define DEFINE_SYSREG_RW_FUNCS(_name)  
    DEFINE_SYSREG_READ_FUNC(_name, _name)  
    DEFINE_SYSREG_WRITE_FUNC(_name, _name)
```

```
#define DEFINE_SYSREG_READ_FUNC(_name, _reg_name)  
static inline u_register_t read_##_name(void)  
{  
    return host_read_sysreg(#_name);  
}
```



Fake_host build and run

```
Activities  Tilix  Wed Nov 16 18:24:48  9.7°C  100%
Tilix: javalm01@e121835: ~
1:javalm01@e121835: ~
javalm01@e121835:~/dev/tf-rmm$ recordmydesktop --no-sound &
[1] 504372
javalm01@e121835:~/dev/tf-rmm$ Initial recording window is set to:
X:0  Y:0  Width:1920  Height:1080
Adjusted recording window is set to:
X:0  Y:4  Width:1920  Height:1072
Your window manager appears to be GNOME Shell

Detected compositing window manager.
Reverting to full screen capture at every frame.
To disable this check run with --no-wm-check
(though that is not advised, since it will probably produce faulty results).

Initializing...
Capturing!
█

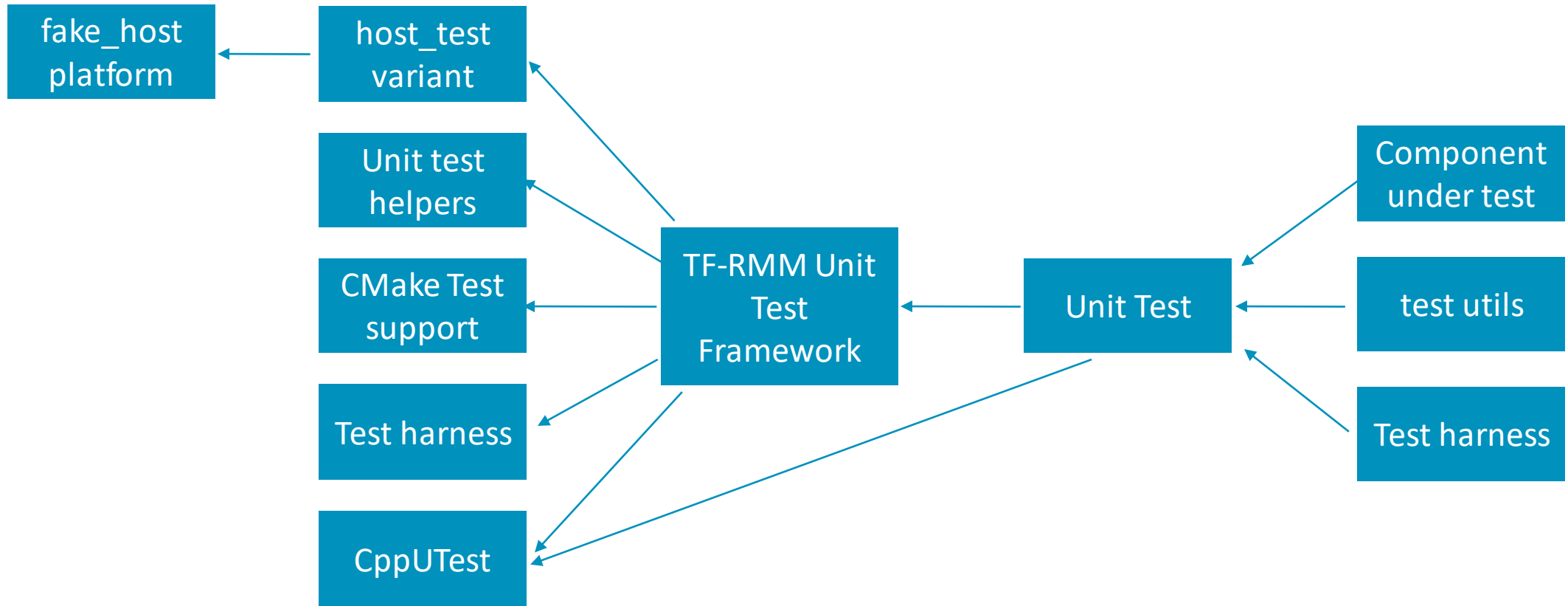
[videos] 0:bash*  "e121835" 18:24 16-Nov-22
```



arm

TF-RMM Unit Test Framework

Unittest framework on RMM



Unittest framework on RMM

Components: CppUtest Framework

- + C/C++ base unit xUnit test framework for unit testing
 - Simple in design and, more importantly, in use
 - Portable
 - Built with test driven mentality for test driven development
 - Added as a git submodule. Automatically linked when the host_test variant is chosen.
 - For more information: <http://cpputest.github.io/>

Unittest framework on RMM

Components: test_helpers.h

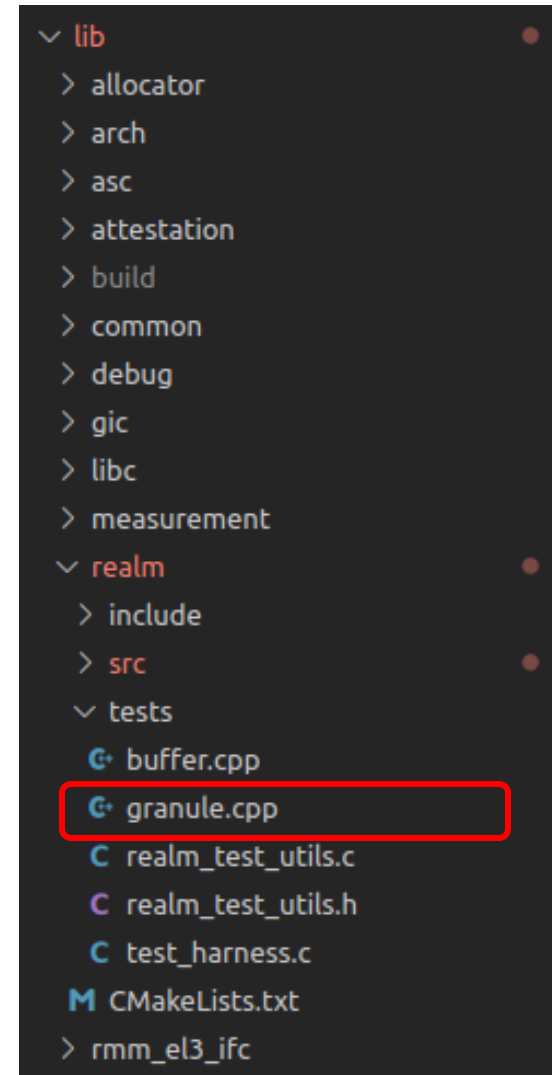
- + Collection of APIs to assist with the tests.
- Very limited at the moment, can be expanded as needed

```
/*  
 * SPDX-License-Identifier: BSD-3-Clause  
 * SPDX-FileCopyrightText: Copyright TF-RMM Contributors.  
 */  
  
#ifndef TEST_HELPERS_H  
#define TEST_HELPERS_H  
  
void test_platform_setup(bool secondaries);  
unsigned int test_get_platform_nr_granules(void);  
  
#endif
```

Test example

Granule test module

- + Include a "tests" directory to hold all the tests for a given library along with ancillary sources.
- + granule.c is part of lib/realm (src/granule.c), therefore granule.cpp will be inside the "tests" directory for lib/realm
 - We are only interested in the public API for this component
- + CMakeLists.txt needs to add support to build and run the tests



Test example

Granule test module

```
70 # Add test functionality
71 rmm_build_unittest(NAME granule
72 | | | | TARGET rmm-lib-realm
73 | | | | SOURCES "tests/granule.cpp"
74 | | | | | "tests/realm_test_utils.c"
75 | | | | | "tests/test_harness.c"
76 | | | | ITERATIONS 1000)
77
78 rmm_build_unittest(NAME slot_buffer
79 | | | | TARGET rmm-lib-realm
80 | | | | SOURCES "tests/buffer.cpp"
81 | | | | | "tests/realm_test_utils.c"
82 | | | | | "tests/test_harness.c"
83 | | | | ITERATIONS 200)
84
```

```
50 TEST_GROUP(granule) {
51
52
53     TEST_SETUP()
54     {
55         static int random_seed = 0;
56
57         if (random_seed == 0) {
58             /* Enable the platform with support for multiple PEs */
59             test_platform_setup(true);
60         }
61     }
62 }
```


Test example

Granule test module

```
50 TEST_GROUP(granule) {
51
52
53     TEST_SETUP()
54     {
55         static int random_seed = 0;
56
57         if (random_seed == 0) {
58             /* Enable the platform with support for multiple PEs */
59             test_platform_setup(true);
60         }
61
62         /* Make sure current cpu id is 0 (primary processor) */
63         host_set_cpuid(0U);
64
65         /* Initialize the random seed */
66         while (random_seed == 0) {
67             random_seed = (int)time(NULL);
68             srand(random_seed);
69         }
70
71         /*
72          * Clean RMM's internal struct granule array
73          * to be sure tests start with a fresh copy.
74          */
75         memset((void *)get_granule_struct(), 0,
76              sizeof(struct granule) *
77              test_get_platform_nr_granules());
78     }
79
80     TEST_TEARDOWN()
81     {}
82 };
```

Test example

Granule test module

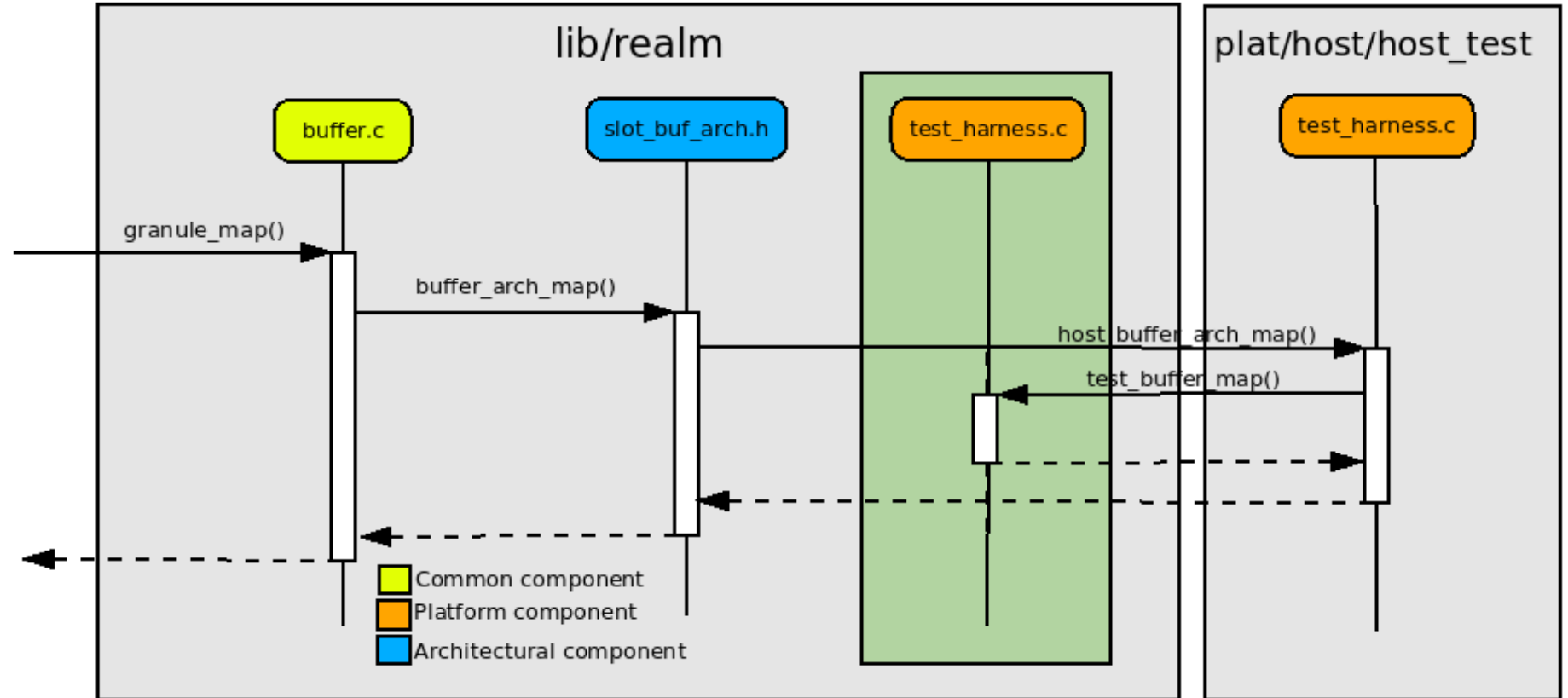
```
202 TEST(granule, find_granule_TC1)
203 {
204     struct granule *expected_granule = get_granule_struct();
205     int granule_index = get_rand_in_range(0,
206                                     test_get_platform_nr_granules() - 1);
207     unsigned long address = (granule_index * GRANULE_SIZE) +
208                             host_util_get_granule_base();
209     struct granule *granule;
210
211     /*****
212     * TEST CASE 1:
213     *
214     * Get a random granule and verify that its physical address
215     * matches the calculated one.
216     *****/
217     expected_granule += granule_index; /* Expected address */
218     granule = find_granule(address);
219     POINTERS_EQUAL(expected_granule, granule);
220
221     /* Verify that not other parameters of the granule are altered */
222     CHECK_TEXT(granule->state == 0, "Invalid granule state");
223     CHECK_TEXT(granule->lock.val == 0, "Invalid granule lock status");
224 }
```

```
227 TEST(granule, find_granule_TC2)
228 {
229     unsigned long address;
230     struct granule *granule;
231
232     /*****
233     * TEST CASE 2:
234     *
235     * Try to get a granule for an unaligned address.
236     *****/
237     address = get_rand_granule_addr();
238     address += get_rand_in_range(1, GRANULE_SIZE - 1);
239     granule = find_granule(address);
240     POINTERS_EQUAL(NULL, granule);
241 }
```

Test example

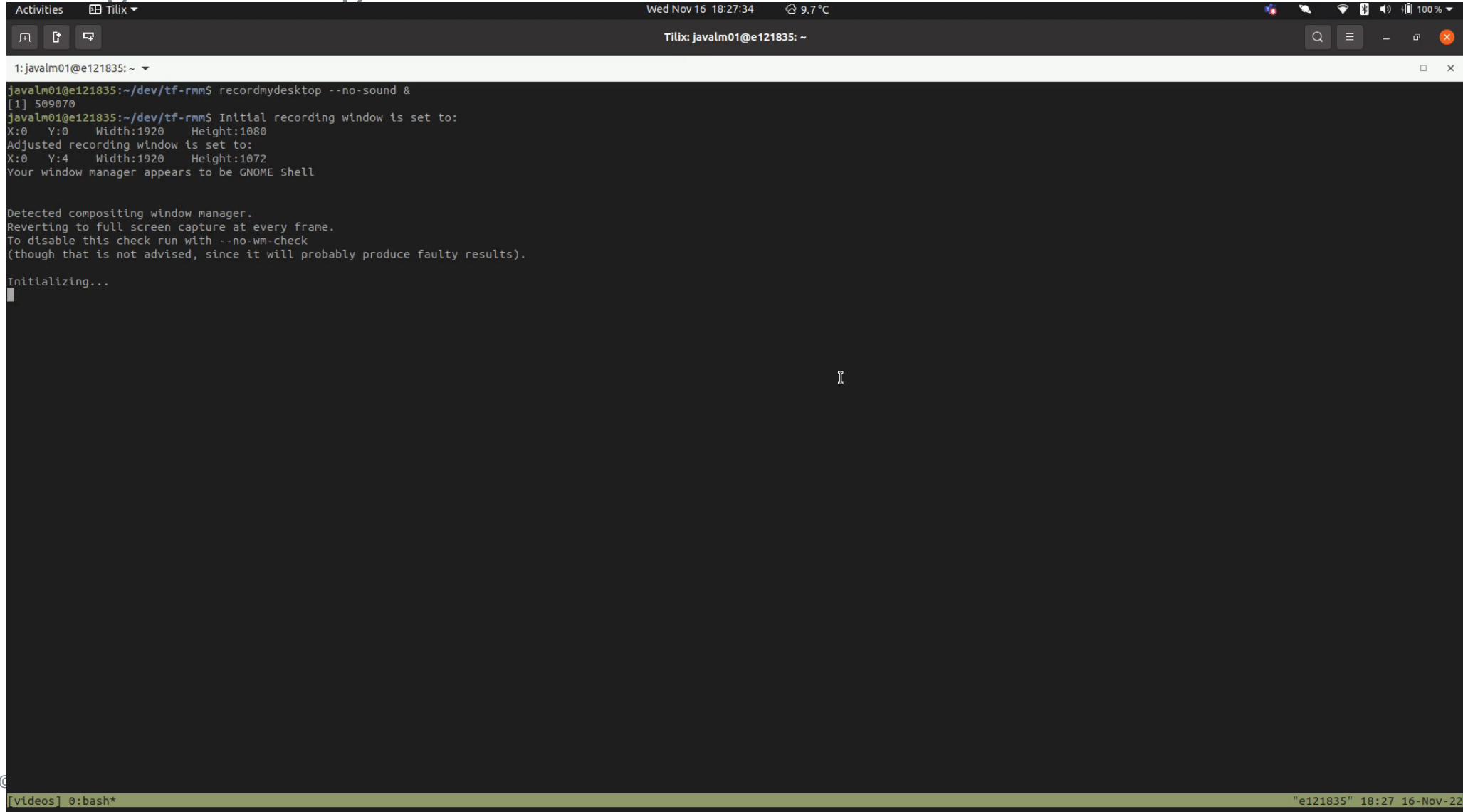
Test harness

```
▼ lib
  > allocator
  > arch
  > asc
  > attestation
  > build
  > common
  > debug
  > gic
  > libc
  > measurement
▼ realm
  > include
  > src
▼ tests
  C buffer.cpp
  C granule.cpp
  C realm_test_utils.c
  C realm_test_utils.h
  C test_harness.c
  M CMakeLists.txt
  > rmm_el3_ifc
```



Example of use

Building and running the tests



```
Activities Tilix
Wed Nov 16 18:27:34 9.7°C
Tilix: javalm01@e121835: ~
1:javalm01@e121835: ~
javalm01@e121835:~/dev/tf-rmm$ recordmydesktop --no-sound &
[1] 509070
javalm01@e121835:~/dev/tf-rmm$ Initial recording window is set to:
X:0 Y:0 Width:1920 Height:1080
Adjusted recording window is set to:
X:0 Y:4 Width:1920 Height:1072
Your window manager appears to be GNOME Shell

Detected compositing window manager.
Reverting to full screen capture at every frame.
To disable this check run with --no-wm-check
(though that is not advised, since it will probably produce faulty results).

Initializing...
|
```

Example of use

Test logs

- + Test results and logs are stored in `<build_dir>/Testing/Temporary/LastTest.log`
- + If `LOG_LEVEL` is enabled (> 0), the file can be very long

```
Translation tables state:
Max allowed PA: 0xfffffffffff
Max allowed VA: 0x0
Max mapped PA: 0xffff - Estimated (transient region)
Max mapped VA: 0xfffffffffff
Initial lookup level: 3
Entries @initial lookup level: 16
Used 0 tables out of 0 (spare: 0)
  [LV3] VA:0xfffffffffff0000 size:0x1000
  [LV3] (15 invalid descriptors omitted)
xlat tables configured for CPU[14]
mmap:
VA:0xfffffffffff0000 PA: TRANSIENT size:0x10000 granularity:0x1000

Translation tables state:
Max allowed PA: 0xfffffffffff
Max allowed VA: 0x0
Max mapped PA: 0xffff - Estimated (transient region)
Max mapped VA: 0xfffffffffff
Initial lookup level: 3
Entries @initial lookup level: 16
Used 0 tables out of 0 (spare: 0)
  [LV3] VA:0xfffffffffff0000 size:0x1000
  [LV3] (15 invalid descriptors omitted)
xlat tables configured for CPU[15]
.
OK (29 tests, 7 ran, 4422 checks, 0 ignored, 22 filtered out, 10 ms)

<end of output>
Test time = 37.10 sec
-----
Test Passed.
"slot_buffer" end time: Aug 19 14:10 BST
"slot_buffer" time elapsed: 00:00:37
-----

End testing: Aug 19 14:10 BST
```

Coverage Report (WIP)

GCC Code Coverage Report

Directory: lib/realm/
 Date: 2022-11-11 17:34:15
 Coverage: low: ≥ 0% medium: ≥ 75.0% high: ≥ 90.0%

	Exec	Total	Coverage
Lines:	607	618	98.2%
Functions:	145	146	99.3%
Branches:	376	1112	33.8%

list of functions

File	Lines	Functions	Branches
include/granule.h	95.3% 82 / 86	100.0% 12 / 12	41.7% 25 / 60
src/granule.c	97.0% 64 / 66	90.0% 9 / 10	87.5% 35 / 40
tests/granule.cpp	98.9% 461 / 466	100.0% 124 / 124	31.2% 316 / 1012

Generated by: [GCOVR \(Version 5.2\)](#)

```

156  /*
157  * Find a set of granules and lock them in order of their address.
158  *
159  * @granules: Pointer to array of @n items. Each item must be pre-populated
160  * with ->addr set to the granule's address, and ->state set to
161  * the expected state of the granule, and ->g_ret pointing to
162  * a valid 'struct granule *'.
163  * This function sorts the supplied array in place.
164  * @n: Number of struct granule_set in array pointed to by @granules
165  *
166  * Returns:
167  * True if all granules in @granules were successfully locked.
168  *
169  * False if any two entries in @granules have the same ->addr, or
170  * if, for any entry in @granules, any of the following is true:
171  * - entry->addr is not aligned to the size of a granule
172  * - entry->addr is out of range
173  * - the state of the granule at entry->addr is not entry->state
174  *
175  * Locking only succeeds if the granules are in their expected states as per the
176  * locking rules in granule_types.h.
177  *
178  * If the function succeeds, for all items in @granules, ->g points to a locked
179  * granule in ->state and *->g_ret is set to the pointer value.
180  *
181  * If the function fails, no lock is held and no *->g_ret pointers are
182  * modified.
183  */
184
185  56000 static bool find_lock_granules(struct granule_set *granules,
186  unsigned long n)
187  {
188  long i;
189
190  168000 for (i = 0; i < n; i++) {
191  112000 granules[i].idx = i;
192  }
193
194  56000 sort_granules(granules, n);
195
196  2/2 122970 for (i = 0; i < n; i++) {
197  /* Check for duplicates */
198  65970 if ((i > 0) && (granules[i].addr == granules[i - 1].addr)) {
199  1000 goto out_err;
200  }
201
202  129940 granules[i].g = find_lock_granule(granules[i].addr,
203  64970 granules[i].state);
204  2/2 64970 if (granules[i].g == NULL) {
205  54000 goto out_err;
206  }
207
208
209  2/2 3000 for (i = 0; i < n; i++) {
210  2000 granules[i].g_ret = granules[i].g;
211  }
212
213  return true;
214
215  56000 out_err:
216  2/2 63970 for (i = i - 1; i >= 0; i--) {
217  17940 granule_unlock(granules[i].g);
218  }
219
220  return false;
221
222  }
223

```

```

84  1/2 6000 TEST(granule, addr_to_granule_TC1)
85  {
86  struct granule *granule;
87  struct granule *expected_granule = get_granule_struct();
88  4000 int granule_index = get_rand_in_range(0,
89  2000 test_get_platform_nr_granules() - 1);
90  2000 unsigned long addr = (granule_index * GRANULE_SIZE) +
91  2000 host_util_get_granule_base();
92
93  /*
94  * TEST CASE 1:
95  *
96  * Verify the granule address for a valid physical address.
97  *
98  *
99  *
100  *
101  1/2 2000 expected_granule += granule_index; /* Expected granule address */
102  2000 granule = addr_to_granule(addr);
103  2000 POINTERS_EQUAL(expected_granule, granule);
104
105  /*
106  * addr_to_granule() asserts if the addr is a NULL pointer, if the
107  * alignment is not correct or if the address is outside of the valid
108  * range, so skip these tests.
109  */
110  }

```

+ For more information: <https://tf-rmm.readthedocs.io/en/latest/>

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Thank You

Danke

Gracias

Grazie

谢谢

ありがとう

Asante

Merci

감사합니다

धन्यवाद

Kiitos

شكراً

ধন্যবাদ

תודה



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